GRADE A

MESC TECHNICAL NEWS

No. M7700-63-9912

Corrections and Supplementary Explanation for "7702/7703 Group User's Manual" (REV. B)

This news includes a few corrections and supplementary explanation for "7702/7703 Group User's Manual".

And also, this news includes the information previously announced by the MESC TECHNICAL NEWS (No. M7700-34-9803, Corrections and Supplementary Explanation for "7702/7703 Group User's Manual" REV. A). ★ represents the new information.

The information about the product expansion, electrical characteristics, and development support tools will not be announced by the MESC TECHNICAL NEWS, even if the above information is updated.
So, for the product expansion, electrical characteristics, and development support tools, please refer to the latest version of the following documents in our web site:

- Product Expansion
  Mitsubishi Microcomputers General Catalog❋
- Electrical Characteristics
  Datasheets
- Development Support Tools
  Datasheets
  Microcomputers Development Support Tools Catalog❋
  Microcomputers Development Support Tools Accessory Guide

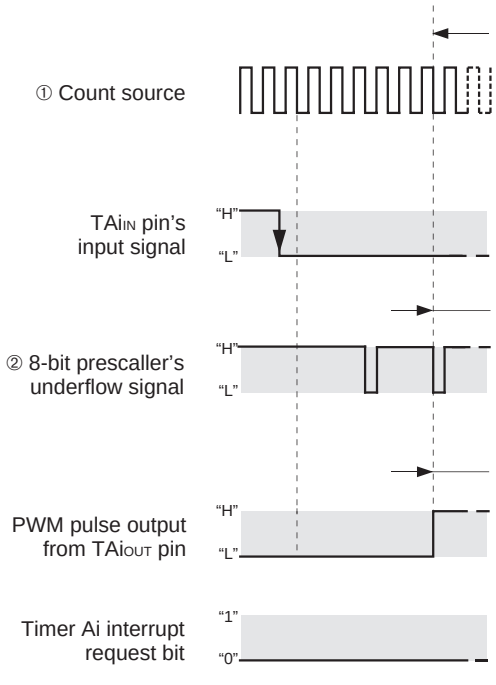
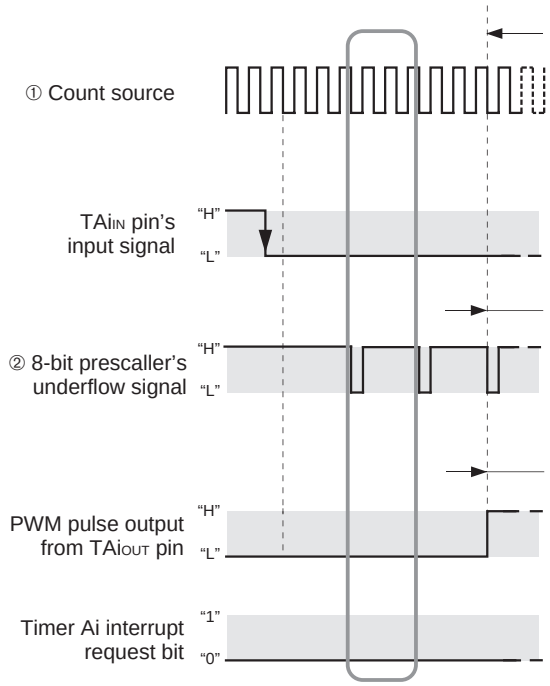
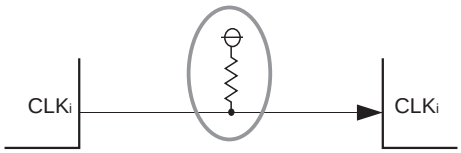
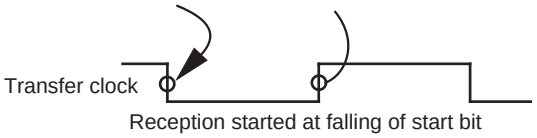
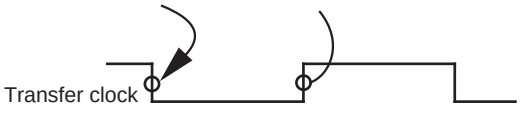
Please Visit Our Web Site.

- Mitsubishi MCU Technical Information (http://www.infomicom.mesc.co.jp/indexe.htm)
- Mitsubishi Microcomputer Development Support Tools
  (http://www.tool-spt.mesc.co.jp/index_e.htm)

❋ The printed version is also released.

Note: For products not included in the above web site, refer to "1996 MITSUBISHI SEMICONDUCTORS DATA BOOK ⟨SINGLE-CHIP 16-BIT MICROCOMPUTERS⟩ Vol. 1 to 2."

Corrections and Supplementary Explanation for “7702/7703 Group User’s Manual” (REV. B) No. 1

	Page	Error	Correction
	P2-8 (2) Bit 1: Zero flag (Z)	**Note**: this flag is invalid in the decimal mode addition (the **ADC** instruction).	**Note**: this flag is invalid in the decimal mode addition (the **ADC** instruction) and subtraction (the **SBC** instruction).
★	P5-44 Note 2	••• the TAjOUT pin outputs “L” level of the PWM pulse which has the same width as set “H” level of the PWM pulse after a trigger generated. •••	••• the TAjOUT pin outputs “L” level for a period of (1/fi) × (m + 1) × (n + 1) after a trigger generated. •••
★	P5-46 Fig. 5.6.6		
	P7-30 Fig. 7.3.9		
	P7-49 Line 2	...and reception starts at detecting ST.	...and the transfer clock is generated at detecting ST, and then reception starts.
	P7-50 Fig. 7.4.11	 Reception started at falling of start bit	 The transfer clock is generated at falling of start bit, and reception starts.
★	P9-8 Line 6	2. When the **STP** instruction (refer to **“Chapter 10. STOP MODE”**) is executed, Watchdog timer stops. When Watchdog timer is used to detect the program runaway, select “**STP** instruction disable” with mask option.	2. When the **STP** instruction (refer to **“CHAPTER 10. STOP MODE”**) is executed, Watchdog timer stops. Unexpected execution of the **STP** instruction code (DB_{16}) owing to a program runaway causes Watchdog timer to stop. Therefore, when Watchdog timer is used to detect the program runaway, we recommend the user to select “**STP** instruction disabled” with “**STP** instruction option” on “MASK ROM ORDER CONFIRMATION FORM.”

Corrections and Supplementary Explanation for “7702/7703 Group User’s Manual” (REV. B) No. 2

	Page	Error	Correction
★	P10-4 Last 3 lines before “**Note**”	**(Note)** after ••• MSB becomes “0.” For interrupts not to be accepted, •••	**(Note)** ••• MSB becomes “0.” (When the level sense of an INT_i interrupt is used, an interrupt request is not retained. Therefore, if the level at the INT_i pin is invalid when the watchdog timer’s MSB becomes “0,” the interrupt request is not accepted.) For interrupts not to be accepted, •••
★	P21-43 5. Processing of ports	<Software protection> ● For ports in ••• or not. ● For ports in •••, periodically set the port Pi register. ● Set the port Pi ••• periods.	<Software protection> ● For ports in ••• or not. ● For ports in •••, periodically set (**Note**) the port Pi register. ● Set (**Note**) the port Pi ••• periods. **Note:** Be sure to use the **LDM** or **STA** instruction for the above rewriting.